

REMARKS

Claims 18 and 19 have been canceled without prejudice or disclaimer. Claims 16, 17, 20 and 21 have been amended. Accordingly, claims 16, 17, 20 and 21 are currently pending in the application.

35 U.S.C. §103

Claims 16 and 19-21 stand rejected under 35 U.S.C. §103 as being unpatentable over McDaniel et al in view of Zenda. Claim 17 stands rejected under 35 U.S.C. §103 as being unpatentable over McDaniel et al in view of Zenda and further in view of Bromberg et al. Claim 18 stands rejected under 35 U.S.C. §103 as being unpatentable over McDaniel et al in view of Zenda and further in view of Bromberg and Naito. These rejections are traversed as follows.

It is submitted that the Examiner's attempted combination of references fails to render the pending claims unpatentable. First of all, McDaniel et al do not disclose a "memory having stored therein control data regarding displaying of the image" as now recited in claims 16 and 20. According to McDaniel et al, the CRT 64 is a peripheral of the system 50. In other words, PROM module 60 including a PROM 140 for address information (see Fig. 3) is contained in a structure other

than the body of CRT 64. Thus, the CRT disclosed by McDaniel et al does not include a memory which stores data regarding the display apparatus. Furthermore, in column 5, lines 20-23 of McDaniel et al, it is stated that "PROM 140 is programmed with the operating program of the control system 50 in a known manner to execute a set of determinable functions and serves to initialize the system's parameters". Therefore, McDaniel et al's PROM 140 stores system parameters and not data regarding the display apparatus. In short, the PROM 140 disclosed by McDaniel et al corresponds in no way whatsoever to the memory of the presently claimed invention.

In addition, McDaniel et al also fail to disclose a "receiver" as recited in the pending claims. As mentioned above, the PROM module 60 and peripheral control module 62 disclosed by McDaniel et al are not contained within the CRT 64 (see Fig. 1). In addition, with respect to Fig. 4 of McDaniel et al, the signal inputted into CRT 178 is designated as composite video, which at best corresponds to the "video signal" of the pending claims, not the "data signal". McDaniel et al do not disclose providing any control signal to read data from the memory, nor that such a control signal includes an instruction for reading out the data from the

memory and is generated based on software for operating the computer.

At column 6, lines 45-51 of McDaniel et al, it is clearly stated that "the CRT controller 158 takes the character information provided to it from the DMA controller 152 and the data bus 56 and generates the necessary line signal commands and character codes to the decoder PROM 164 and character PROM 166 to effect the generation of the pre-specified series of dot characters on the face of the CRT 178". Therefore, these line signal commands and character codes are used to generate a video signal for displaying dot characters and do not represent a control signal for reading out data from the memory as recited in the pending claims. As such, it should be clear that McDaniel et al clearly fail to disclose the "receiver" of the present invention.

CRT 64 of McDaniel et al seems to be connected to the peripheral control module 62 with an arrow which is not bi-directional. This suggests that no data is sent from the CRT 64 to the peripheral control module 62. On the other hand, keyboard 66 and teletype 68 are connected with bi-directional lines as shown in Fig. 1. Furthermore, at column 5, lines 29-33 of McDaniel et al, it is stated that the "PROM module 60 further includes acknowledge logic 148 which generates an

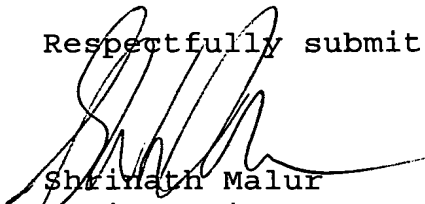
acknowledged signal which is sent to the processor 90 to acknowledge to the processor 90 that good data is present on the data bus". Therefore, McDaniel et al use an acknowledge signal merely to indicate the status of data on a data bus 56, as opposed to acknowledging receipt of a control signal at a receiver.

The deficiencies in the primary references are not overcome by resort to the other cited references. Therefore, it is submitted that the pending claims patentably define the present invention over the cited art.

CONCLUSION

In view of the foregoing amendments and remarks, Applicants contend that the above-identified application is now in condition for allowance. Accordingly, reconsideration and reexamination are respectfully requested.

Respectfully submitted,



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MARKED UP VERSION OF REWRITTEN CLAIMS

16. (Amended) A display unit for displaying an image based on a video signal received from [a personal] an external computer which is connected to an input device, comprising:

a receiving/transmitting unit receiving from said computer a first control signal generated in response to a control instruction inputted through input means of said computer and a second control signal generated based on a program running on said computer and transmitting an acknowledge signal to said computer to inform that said first and second control signals are received; and

a memory having stored therein control data regarding displaying of the image;

wherein the image is controlled and displayed by using said input means of said computer based on said first control signal and said control data is read from said memory in response to said second control signal thereby to control the image [an interface circuit which receives from said personal computer said video signal and a control signal including control data corresponding to a command inputted in said personal computer;

a decoder which decodes said control data included in said control signal to provide a digital signal; and

an adjustment signal generation circuit which generates an adjustment signal for a size and/or position of said displayed image according to said digital signal provided from said decoder;

wherein said interface circuit upon receiving said control signal sends out an acknowledge signal indicating reception of said control signal to said personal computer].

17. (Amended) A display unit according to claim 16, wherein said program is previously prepared in a software for said computer [control signal further includes a start bit, a control code and a stop bit].

20. (Amended) A display unit for displaying an image based on a video signal received from [a personal] an external computer [which is connected to an input device], comprising:
a receiver receiving from said computer a first control signal generated in response to a control instruction inputted through input means of said computer and a second control signal generated based on a program running on said computer;

a transmitter transmitting an acknowledge signal to said computer to inform that said first and second control signals are received; and

a memory having stored therein control data regarding displaying of the image;

wherein the image is controlled and displayed by using said input means of said computer based on said first control signal and said control data is read from said memory in response to said second control signal thereby to control the image [an interface circuit which receives from said personal computer said video signal and a control signal including control data corresponding to a command inputted in said personal computer, and transmits an acknowledge signal indicating reception of said control signal to said personal computer;

a decoder which decodes said control data included in said control signal to provide a digital signal; and

an adjustment signal generation circuit which generates an adjustment signal for a size and/or position of said displayed image according to said digital signal provided from said decoder,

wherein said input device adjusts said size and/or position of said displayed image based on said adjustment

signal generated from said adjustment signal generation
circuit independent of any manual adjustment switch
instrument of said display unit].

21. (Amended) A display unit according to claim 20,
wherein said input [device] means includes a keyboard.